

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	"710"/\$4.ccls. and (I/O or input with output) adj2 processor\$4 same manag\$4 adj3 server and ((fail adj safe or failsafe) same tim\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:12
L2	0	"714"/\$4.ccls. and (I/O or input with output) adj2 processor\$4 same manag\$4 adj3 server and ((fail adj safe or failsafe) same tim\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:14
L3	0	"714"/\$4.ccls. and (I/O or input with output) adj2 processor\$4 same manag\$4 adj3 server same tim\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:14
L4	0	"714"/\$4.ccls. and (I/O or (input with output)) adj2 processor\$4 same manag\$4 adj3 server same tim\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:14
L5	0	"710"/\$4.ccls. and (I/O or (input with output)) adj2 processor\$4 same manag\$4 adj3 server same tim\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:15
L6	9	"710"/\$4.ccls. and (I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:32
L7	7	"714"/\$4.ccls. and (I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:41
L8	23	processor\$2 and ((fail adj safe or failsafe) with timer) and count\$4 and registe\$6 and (threshold\$4 or expire\$4 or exceed\$4) and (fail adj safe or failsafe) with reset\$4 and boot\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:34
L9	0	"710"/\$4.ccls. and (I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:32

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L10	0	"710"/\$4.ccls. and (I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4 and register\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:32
L11	0	"714"/\$4.ccls. and (I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4 and register\$4 and lock and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:33
L12	0	(I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4 and register\$4 and lock and unclock\$4 and failsafe\$4 and reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:36
L13	0	(I/O or (input with output))and remot\$4 same manag\$4 adj3 server same tim\$4 and register\$4 and lock and unclock\$4 and failsafe\$4 and reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:34
L14	0	(I/O or (input with output))and remot\$4 same manag\$4 adj3 server and register\$4 and lock and unclock\$4 and failsafe\$4 and reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:34
L15	0	processor\$2 and ((fail adj safe or failsafe) with timer) and count\$4 and registe\$6 and (threshold\$4 or expire\$4 or exceed\$4) and (fail adj safe or failsafe) with reset\$4 and boot\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:34
L16	0	processor\$2 and ((fail adj safe or failsafe) with timer) and count\$4 and registe\$6 and (threshold\$4 or expire\$4 or exceed\$4) and (fail adj safe or failsafe) with reset\$4 and boot\$4 and lock\$4 and unclock\$4 and register\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:35
L17	0	processor\$2 and ((fail adj safe or failsafe) with timer) and count\$4 and registe\$6 and (threshold\$4 or expire\$4 or exceed\$4) and reset\$4 and boot\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:35
L18	0	processor\$2 and ((fail adj safe or failsafe) with timer) and registe\$6 and (threshold\$4 or expire\$4 or exceed\$4) and reset\$4 and boot\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:35

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L19	0	processor\$2 and ((fail adj safe or failsafe) with timer) and registe\$6 and reset\$4 and boot\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:36
L20	0	proccesor\$2 and ((fail adj safe or failsafe) with timer) and registe\$6 and reset\$4 and boot\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:36
L21	0	processor\$2 and (fail adj safe or failsafe) and registe\$6 and reset\$4 and boot\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:36
L22	0	(I/O or (input with output))and manag\$4 adj3 server and register\$4 and lock and unclock\$4 and failsafe\$4 and reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:37
L23	0	(I/O or (input with output))and manag\$4 adj3 server and register\$4 and lock and unclock\$4 and reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:37
L24	0	710/200.ccls. and (I/O or (input with output))and manag\$4 adj3 server and register\$4 and lock and unclock\$4 and reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:37
L25	635	710/200.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:38
L26	1184	710/1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:38
L27	0	8 and 25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:38
L28	0	9 and 25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:38

EAST Search History

L29	0	8 and 26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:38
L30	0	9 and 26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:38
L31	0	"714"/\$4.ccls. and (I/O or (input with output))and manag\$4 adj3 server and tim\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:41
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L8	23	processor\$2 and ((fail adj safe or failsafe) with timer) and count\$4 and registe\$6 and (threshold\$4 or expire\$4 or exceed\$4) and (fail adj safe or failsafe) with reset\$4 and boot\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:44
L9	0	"710"/\$4.ccls. and (I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:32

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L10	0	"710"/\$4.ccls. and (I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4 and register\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:32
L11	0	"714"/\$4.ccls. and (I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4 and register\$4 and lock and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:33
L12	0	(I/O or (input with output))and remot\$4 with manag\$6 same manag\$4 adj3 server same tim\$4 and register\$4 and lock and unclock\$4 and failsafe\$4 and reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:36
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L26	1184	710/1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:38
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L33	245	"714"/12.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:43
L34	142	"714"/702.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:43
L35	374	"714"/734.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:44
L36	1005	"714"/763.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:44
L37	28	processor\$2 and ((fail adj safe or failsafe) with timer) and registe\$6 and (fail adj safe or failsafe) with reset\$4 and boot\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:45
L38	67	processor\$2 and ((fail adj safe or failsafe) with timer) and registe\$6 and (fail adj safe or failsafe) with reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:45

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L39	0	processor\$2 and ((fail adj safe or failsafe) with timer) and registe\$6 and (fail adj safe or failsafe) with reset\$4 and lock\$4 and unclock\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:45
L40	1	33 and 38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:46
L41	0	34 and 38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:46
L42	0	35 and 38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:46
L43	0	36 and 38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:46
L44	28	37 and 38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/11 18:46



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Relevance scale

1 A cost-effective, high-bandwidth storage architecture

Garth A. Gibson, David F. Nagle, Khalil Amiri, Jeff Butler, Fay W. Chang, Howard Gobioff, Charles Hardin, Erik Riedel, David Rochberg, Jim Zelenka
October 1998 **ACM SIGOPS Operating Systems Review , ACM SIGPLAN Notices , Proceedings of the eighth international conference on Architectural support for programming languages and operating systems ASPLOS-VIII**, Volume 32 , 33 Issue 5 , 11

Publisher: ACM Press

Full text available: [pdf\(1.67 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes the Network-Attached Secure Disk (NASD) storage architecture, prototype implementations of NASD drives, array management for our architecture, and three, filesystems built on our prototype. NASD provides scalable storage bandwidth without the cost of servers used primarily, for transferring data from peripheral networks (e.g. SCSI) to client networks (e.g. ethernet). Increasing dataset sizes, new attachment technologies, the convergence of peripheral and interprocessor switch ...

2 A decade of reconfigurable computing: a visionary retrospective

R. Hartenstein
March 2001 **Proceedings of the conference on Design, automation and test in Europe**

Publisher: IEEE Press

Full text available: [pdf\(768.00 KB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

3 A framework for the assessment of operating systems for small computers

Hossein Saiedian, Munib Siddiqi
April 1996 **ACM SIGICE Bulletin**, Volume 21 Issue 4

Publisher: ACM Press

Full text available: [pdf\(1.89 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A number of high performance operating systems are now available for small computers on different hardware platforms. These operating systems offer many advanced features formerly reserved for their workstation and minicomputer counterparts. This article surveys the most widely used of such operating systems, namely OS/2, Windows NT, Linux and Macintosh System 7.5. It provides an account on the history, design objectives and evolution of these operating systems and discusses their key features, ...

Keywords: CP/M, DOS, Linux, Macintosh, Microcomputers, OS/2, Operating Systems, Small Computer Systems, Windows, Windows NT

4 A history of the Promis technology: an effective human interface

 Jan Schultz

January 1986 **Proceedings of the ACM Conference on The history of personal workstations**

Publisher: ACM Press

Full text available:  pdf(2.61 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Scientific computing systems for individuals were pioneered early at Hewlett-Packard, beginning with the 9100A Desktop Calculator in 1968. Extensions of this first machine were soon seen in Personal Peripherals, such as Printers, Tape Cartridges, and Plotters, and followed by Graphic CRT Displays. By early 1972, the Desktop unit had been augmented by a very powerful Pocket Calculator, the ground-breaking HP 35A. This paper traces the evolution of these machines to the present day, ...

5 A model of real time control system production

 M. N. Matelan

June 1976 **ACM SIGDA Newsletter**, Volume 6 Issue 2

Publisher: ACM Press

Full text available:  pdf(1.75 MB) Additional Information: [full citation](#), [abstract](#), [references](#)

Many facets of Computer Science and associated technologies may be profitably viewed as dedicated real time control activities. Production of systems to exercise such control has been difficult and costly. An abstract model of the process of producing these systems is presented. The model indicates three areas of the design problem amenable to automation: 1) the selection and configuration of hardware; 2) the production of software; and 3) the selection of a monitor to maintain real time integri ...

6 A taxonomy of computer program security flaws

 Carl E. Landwehr, Alan R. Bull, John P. McDermott, William S. Choi

September 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 3

Publisher: ACM Press

Full text available:  pdf(3.81 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

An organized record of actual flaws can be useful to computer system designers, programmers, analysts, administrators, and users. This survey provides a taxonomy for computer program security flaws, with an Appendix that documents 50 actual security flaws. These flaws have all been described previously in the open literature, but in widely separated places. For those new to the field of computer security, they provide a good introduction to the characteristics of security flaws and how they ...

Keywords: error/defect classification, security flaw, taxonomy

7 altPm: a strategy for integrating IP with ATM

 Guru Parulkar, Douglas C. Schmidt, Jonathan S. Turner

October 1995 **ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Applications, technologies, architectures, and protocols for computer communication SIGCOMM '95**, Volume 25 Issue 4

Publisher: ACM Press

Full text available:  pdf(1.17 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)

terms

This paper describes research on new methods and architectures that enable the synergistic combination of IP and ATM technologies. We have designed a highly scalable gigabit IP router based on an ATM core and a set of tightly coupled general-purpose processors. This a^tt^pm (pronounced "IP on ATM" or, if you prefer, "ip-attem") architecture provides flexibility in congestion control, routing, resource management, and packe ...

8 ABSTRACTS OF INTEREST

 Ben Shneiderman
October 1992 **ACM SIGCHI Bulletin**, Volume 24 Issue 4

Publisher: ACM Press

Full text available:  pdf(1.83 MB) Additional Information: [full citation](#), [abstract](#)

The following abstracts were selected from a computer search using the BRS Information Technologies retrieval services of the Dissertation Abstracts International (DAI) database produced by University Microfilms International. Unless otherwise specified, paper or microform copies of dissertations may be ordered, using the UMI order number, from University Microfilms International, Dissertation Copies, Post Office Box 1794, Ann Arbor, MI 488106; telephone for U.S. (except Michigan, Hawaii, or Alask ...

9 Active base stations and nodes for wireless networks

Athanassios Boulis, Paul Lettieri, Mani Srivastava
January 2003 **Wireless Networks**, Volume 9 Issue 1

Publisher: Kluwer Academic Publishers

Full text available:  pdf(441.19 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Mobile and wireless network systems are characterized by a highly time varying and heterogeneous operational environment. For example, the wireless link bandwidth and bit error rate can change due to fading, mobile nodes may have different capabilities, and in the course of its movements a mobile node may visit base stations that provide different sets of services, protocols, and interfaces. Adaptability, in various forms and at various levels of the system, is a key to combating the inherent va ...

Keywords: active networking, base station, reconfigurable hardware, wireless and mobile nodes

10 Active disks: programming model, algorithms and evaluation

 Anurag Acharya, Mustafa Uysal, Joel Saltz
October 1998 **ACM SIGPLAN Notices , ACM SIGOPS Operating Systems Review , Proceedings of the eighth international conference on Architectural support for programming languages and operating systems ASPLOS-VIII**, Volume 33 , 32 Issue 11 , 5

Publisher: ACM Press

Full text available:  pdf(1.57 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Several application and technology trends indicate that it might be both profitable and feasible to move computation closer to the data that it processes. In this paper, we evaluate *Active Disk* architectures which integrate significant processing power and memory into a disk drive and allow application-specific code to be downloaded and executed on the data that is being read from (written to) disk. The key idea is to offload bulk of the processing to the diskresident processors and to us ...

11

Ada software experience with the advanced information processing system

L. S. Alger

July 1989 **Proceedings of the conference on TRI-Ada '88**

Publisher: ACM Press

Full text available: [pdf\(1.35 MB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

The topics covered by the presentation are listed: The AIPS system overview is informational and will be covered quickly AIPS hardware and software are also informational and will be covered quickly Experience include enhancements to the Ada run time system, and the problems and advantages of using Ada for real time, fault tolerant systems Benchmarks were done on several processors and several run time ...

12 Algorithms for scalable synchronization on shared-memory multiprocessors

John M. Mellor-Crummey, Michael L. Scott

February 1991 **ACM Transactions on Computer Systems (TOCS)**, Volume 9 Issue 1

Publisher: ACM Press

Full text available: [pdf\(3.07 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Busy-wait techniques are heavily used for mutual exclusion and barrier synchronization in shared-memory parallel programs. Unfortunately, typical implementations of busy-waiting tend to produce large amounts of memory and interconnect contention, introducing performance bottlenecks that become markedly more pronounced as applications scale. We argue that this problem is not fundamental, and that one can in fact construct busy-wait synchronization algorithms that induce no memory or interc ...

13 An application level video gateway

Elan Amir, Steven McCanne, Hui Zhang

January 1995 **Proceedings of the third ACM international conference on Multimedia**

Publisher: ACM Press

Full text available: [htm\(54.34 KB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: conferencing protocols, digital video, efficient transcoding, image and video compression and processing, multicasting, networking and communication

14 An Architecture for the Integration of Physical and Informational Spaces

Scott M. Thayer, Peter Steenkiste

July 2003 **Personal and Ubiquitous Computing**, Volume 7 Issue 2

Publisher: Springer-Verlag

Full text available: [pdf\(328.25 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

AbstractWhile computer processing power, storage capacity, and bandwidth are continuing to experience exponential growth, individual human processing capabilities are not increasing significantly. Pervasive computing offers an opportunity for applications to interact with the physical environment and to provide a task-centric and mobile infrastructure for the user. However, this rich environment can also be distracting, in part because of a lack of convergence between the physical infrastructure ...

Keywords: Context-aware computing, Distraction-free, Multi-modal input-output, Pervasive computing

15 An input/output subsystem for the Hawk operating system kernel

David L. Harris

April 1988 **ACM SIGOPS Operating Systems Review**, Volume 22 Issue 2

Publisher: ACM Press

Full text available: [pdf\(662.23 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

This paper describes the input/output subsystem that is provided with the Hawk operating system kernel and the SANDAC V computer. Hawk and the SANDAC V were designed for embedded hard real-time applications. The IOS is a hierarchically organized set of modules that can be configured to match the requirements of a specific real-time application. It was designed to interact with and control a variety of peripheral devices, some of which do not match traditional models. The subsystem executes co ...

16 An interactive codesign environment for domain-specific coprocessors

Patrick Schaumont, Doris Ching, Ingrid Verbauwhede

January 2006 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 11 Issue 1

Publisher: ACM Press

Full text available: [pdf\(406.61 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Energy-efficient embedded systems rely on domain-specific coprocessors for dedicated tasks such as baseband processing, video coding, or encryption. We present a language and design environment called GEZEL that can be used for the design, verification and implementation of such coprocessor-based systems. The GEZEL environment creates a platform simulator by combining a hardware simulation kernel with one or more instruction-set simulators. The hardware part of the platform is programmed in GEZEL ...

Keywords: Cosimulation, hardware description language, hardware-software codesign

17 An interactive diagnostic/debugging subsystem for bit-slice processors

F. J. Burkowski

December 1985 **ACM SIGMICRO Newsletter , Proceedings of the 18th annual workshop on Microprogramming MICRO 18**, Volume 16 Issue 4

Publisher: ACM Press

Full text available: [pdf\(827.77 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper discusses the design and implementation of a debugging/diagnostic subsystem for a bit-slice processor. The subsystem uses serial shadow registers under the control of a single chip microcomputer both to observe and to control processor behavior. Serial lines link the microcomputer to a diagnostic host which provides the user with a comprehensive set of interactive diagnostic commands. Using these commands, the user is able to load the writable control store, verify its contents, ...

18 An optimal algorithm for minimizing run-time reconfiguration delay

Soheil Ghiasi, Ani Nahapetian, Majid Sarrafzadeh

May 2004 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 3 Issue 2

Publisher: ACM Press

Full text available: [pdf\(1.83 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Reconfiguration delay is one of the major barriers in the way of dynamically adapting a system to its application requirements. The run-time reconfiguration delay is quite comparable to the application latency for many classes of applications and might even dominate the application run-time. In this paper, we present an efficient optimal algorithm for minimizing the run-time reconfiguration (context switching) delay of executing an application on a dynamically adaptable system. The system is com ...

Keywords: Reconfigurable computing, instantiation ordering, reconfiguration delay

19 An overview of embedded system design education at berkeley

 Alberto L. Sangiovanni-Vincentelli, Alessandro Pinto
August 2005 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 4 Issue 3

Publisher: ACM Press

Full text available:  pdf(225.54 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Embedded systems have been a traditional area of strength in the research agenda of the University of California at Berkeley. In parallel to this effort, a pattern of graduate and undergraduate classes has emerged that is the result of a distillation process of the research results. In this paper, we present the considerations that are driving our curriculum development and we review our undergraduate and graduate program. In particular, we describe in detail a graduate class (EECS249: Design of ...

Keywords: Graduate and undergraduate education, architectural design, embedded software, embedded systems, functional design, sourcework

20 An ultra low-power processor for sensor networks

 Virantha Ekanayake, Clinton Kelly, Rajit Manohar
October 2004 **ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Architecture News , ACM SIGPLAN Notices , Proceedings of the 11th international conference on Architectural support for programming languages and operating systems ASPLOS-XI**, Volume 38 , 32 , 39 Issue 5 , 5 , 11

Publisher: ACM Press

Full text available:  pdf(437.23 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We present a novel processor architecture designed specifically for use in low-power wireless sensor-network nodes. Our sensor network asynchronous processor (SNAP/LE) is based on an asynchronous data-driven 16-bit RISC core with an extremely low-power idle state, and a wakeup response latency on the order of tens of nanoseconds. The processor instruction set is optimized for sensor-network applications, with support for event scheduling, pseudo-random number generation, bitfield operations, and ...

Keywords: asynchronous, event-driven, low-energy, picojoule computing, sensor network processor, sensor networks, wireless

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1 Network Protocols



Andrew S. Tanenbaum

December 1981 **ACM Computing Surveys (CSUR)**, Volume 13 Issue 4

Publisher: ACM Press

Full text available: [pdf\(3.37 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



2 Distributed operating systems



Andrew S. Tanenbaum, Robbert Van Renesse

December 1985 **ACM Computing Surveys (CSUR)**, Volume 17 Issue 4

Publisher: ACM Press

Full text available: [pdf\(5.49 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)



Distributed operating systems have many aspects in common with centralized ones, but they also differ in certain ways. This paper is intended as an introduction to distributed operating systems, and especially to current university research about them. After a discussion of what constitutes a distributed operating system and how it is distinguished from a computer network, various key design issues are discussed. Then several examples of current research projects are examined in some detail ...

3 Database Management Systems Development in the USSR



A. G. Dale

September 1979 **ACM Computing Surveys (CSUR)**, Volume 11 Issue 3

Publisher: ACM Press

Full text available: [pdf\(1.34 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



4 Concurrency Control in Distributed Database Systems



Philip A. Bernstein, Nathan Goodman

June 1981 **ACM Computing Surveys (CSUR)**, Volume 13 Issue 2

Publisher: ACM Press

Full text available: [pdf\(3.24 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



5 Formalizing the safety of Java, the Java virtual machine, and Java card

 Pieter H. Hartel, Luc Moreau
December 2001 **ACM Computing Surveys (CSUR)**, Volume 33 Issue 4

Publisher: ACM Press

Full text available:  pdf(442.86 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We review the existing literature on Java safety, emphasizing formal approaches, and the impact of Java safety on small footprint devices such as smartcards. The conclusion is that although a lot of good work has been done, a more concerted effort is needed to build a coherent set of machine-readable formal models of the whole of Java and its implementation. This is a formidable task but we believe it is essential to build trust in Java safety, and thence to achieve ITSEC level 6 or Common Crite ...

Keywords: Common criteria, programming

6 RAID: high-performance, reliable secondary storage

 Peter M. Chen, Edward K. Lee, Garth A. Gibson, Randy H. Katz, David A. Patterson
June 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 2

Publisher: ACM Press

Full text available:  pdf(3.60 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Disk arrays were proposed in the 1980s as a way to use parallelism between multiple disks to improve aggregate I/O performance. Today they appear in the product lines of most major computer manufacturers. This article gives a comprehensive overview of disk arrays and provides a framework in which to organize current and future work. First, the article introduces disk technology and reviews the driving forces that have popularized disk arrays: performance and reliability. It discusses the tw ...

Keywords: RAID, disk array, parallel I/O, redundancy, storage, striping

7 Highly available systems for database applications

 Won Kim
March 1984 **ACM Computing Surveys (CSUR)**, Volume 16 Issue 1

Publisher: ACM Press

Full text available:  pdf(2.43 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

As users entrust more and more of their applications to computer systems, the need for systems that are continuously operational (24 hours per day) has become even greater. This paper presents a survey and analysis of representative architectures and techniques that have been developed for constructing highly available systems for database applications. It then proposes a design of a distributed software subsystem that can serve as a unified framework for constructing database applica ...

8 Heterogeneous distributed database systems for production use

 Gomer Thomas, Glenn R. Thompson, Chin-Wan Chung, Edward Barkmeyer, Fred Carter, Marjorie Templeton, Stephen Fox, Berl Hartman
September 1990 **ACM Computing Surveys (CSUR)**, Volume 22 Issue 3

Publisher: ACM Press

Full text available:  pdf(2.90 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

It is increasingly important for organizations to achieve additional coordination of diverse computerized operations. To do so, it is necessary to have database systems that can operate over a distributed network and can encompass a heterogeneous mix of computers, operating systems, communications links, and local database management systems. This paper outlines approaches to various aspects of heterogeneous distributed data management and describes the characteristics and architectures of ...

9 Software safety: why, what, and how

 Nancy G. Leveson

June 1986 **ACM Computing Surveys (CSUR)**, Volume 18 Issue 2

Publisher: ACM Press

Full text available:  pdf(4.18 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Software safety issues become important when computers are used to control real-time, safety-critical processes. This survey attempts to explain why there is a problem, what the problem is, and what is known about how to solve it. Since this is a relatively new software research area, emphasis is placed on delineating the outstanding issues and research topics.

10 Query evaluation techniques for large databases

 Goetz Graefe

June 1993 **ACM Computing Surveys (CSUR)**, Volume 25 Issue 2

Publisher: ACM Press

Full text available:  pdf(9.37 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Database management systems will continue to manage large data volumes. Thus, efficient algorithms for accessing and manipulating large sets and sequences will be required to provide acceptable performance. The advent of object-oriented and extensible database systems will not solve this problem. On the contrary, modern data models exacerbate the problem: In order to manipulate large sets of complex objects as efficiently as today's database systems manipulate simple records, query-processi ...

Keywords: complex query evaluation plans, dynamic query evaluation plans, extensible database systems, iterators, object-oriented database systems, operator model of parallelization, parallel algorithms, relational database systems, set-matching algorithms, sort-hash duality

11 The family of concurrent logic programming languages

 Ehud Shapiro

September 1989 **ACM Computing Surveys (CSUR)**, Volume 21 Issue 3

Publisher: ACM Press

Full text available:  pdf(9.62 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Concurrent logic languages are high-level programming languages for parallel and distributed systems that offer a wide range of both known and novel concurrent programming techniques. Being logic programming languages, they preserve many advantages of the abstract logic programming model, including the logical reading of programs and computations, the convenience of representing data structures with logical terms and manipulating them using unification, and the amenability to metaprogrammin ...

12

Tools and approaches for developing data-intensive Web applications: a survey

 Piero Fraternali

September 1999 **ACM Computing Surveys (CSUR)**, Volume 31 Issue 3

Publisher: ACM Press

Full text available:  pdf(524.80 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The exponential growth and capillary diffusion of the Web are nurturing a novel generation of applications, characterized by a direct business-to-customer relationship. The development of such applications is a hybrid between traditional IS development and Hypermedia authoring, and challenges the existing tools and approaches for software production. This paper investigates the current situation of Web development tools, both in the commercial and research fields, by identifying and characterizing ...

Keywords: HTML, Intranet, WWW, application, development

13 Compiler transformations for high-performance computing 

 David F. Bacon, Susan L. Graham, Oliver J. Sharp

December 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 4

Publisher: ACM Press

Full text available:  pdf(6.32 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

In the last three decades a large number of compiler transformations for optimizing programs have been implemented. Most optimizations for uniprocessors reduce the number of instructions executed by the program using transformations based on the analysis of scalar quantities and data-flow techniques. In contrast, optimizations for high-performance superscalar, vector, and parallel processors maximize parallelism and memory locality with transformations that rely on tracking the properties of ...

Keywords: compilation, dependence analysis, locality, multiprocessors, optimization, parallelism, superscalar processors, vectorization

14 Types and persistence in database programming languages 

 Malcolm P. Atkinson, O. Peter Buneman

June 1987 **ACM Computing Surveys (CSUR)**, Volume 19 Issue 2

Publisher: ACM Press

Full text available:  pdf(7.91 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Traditionally, the interface between a programming language and a database has either been through a set of relatively low-level subroutine calls, or it has required some form of embedding of one language in another. Recently, the necessity of integrating database and programming language techniques has received some long-overdue recognition. In response, a number of attempts have been made to construct programming languages with completely integrated database management systems. These languages ...

15 A taxonomy of computer program security flaws 

 Carl E. Landwehr, Alan R. Bull, John P. McDermott, William S. Choi

September 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 3

Publisher: ACM Press

Full text available:  pdf(3.81 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

An organized record of actual flaws can be useful to computer system designers, programmers, analysts, administrators, and users. This survey provides a taxonomy for

computer program security flaws, with an Appendix that documents 50 actual security flaws. These flaws have all been described previously in the open literature, but in widely separated places. For those new to the field of computer security, they provide a good introduction to the characteristics of security flaws and how they ...

Keywords: error/defect classification, security flaw, taxonomy

16 On randomization in sequential and distributed algorithms

 Rajiv Gupta, Scott A. Smolka, Shaji Bhaskar
March 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 1

Publisher: ACM Press

Full text available:  pdf(8.01 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Probabilistic; or randomized, algorithms are fast becoming as commonplace as conventional deterministic algorithms. This survey presents five techniques that have been widely used in the design of randomized algorithms. These techniques are illustrated using 12 randomized algorithms—both sequential and distributed—that span a wide range of applications, including: primality testing (a classical problem in number theory), interactive probabilistic proof s ...

Keywords: Byzantine agreement, CSP, analysis of algorithms, computational complexity, dining philosophers problem, distributed algorithms, graph isomorphism, hashing, interactive probabilistic proof systems, leader election, message routing, nearest-neighbors problem, perfect hashing, primality testing, probabilistic techniques, randomized or probabilistic algorithms, randomized quicksort, sequential algorithms, transitive tournaments, universal hashing

17 Power reduction techniques for microprocessor systems

 Vasanth Venkatachalam, Michael Franz
September 2005 **ACM Computing Surveys (CSUR)**, Volume 37 Issue 3

Publisher: ACM Press

Full text available:  pdf(602.33 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Power consumption is a major factor that limits the performance of computers. We survey the "state of the art" in techniques that reduce the total power consumed by a microprocessor system over time. These techniques are applied at various levels ranging from circuits to architectures, architectures to system software, and system software to applications. They also include holistic approaches that will become more important over the next decade. We conclude that power management is a ...

Keywords: Energy dissipation, power reduction

18 Increasing the reliability of control systems with agent technology

 July 2001 **ACM SIGAPP Applied Computing Review**, Volume 9 Issue 2

Publisher: ACM Press

Full text available:  pdf(954.43 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#), [review](#)

This paper describes how Software Agents technology can be used to increase the reliability of a control system. Supervisory Control and Data Acquisition (SCADA) systems are established as means to exercise supervisory control over an industrial process. Software Agent technology can improve SCADA systems as it allows distribution, which inherently promotes redundancy, and modularity, which promotes versatility.

19 Process exchange on the PR1ME family of computers

 Edward A. Feustel

March 1984 **ACM SIGARCH Computer Architecture News**, Volume 12 Issue 1

Publisher: ACM Press

Full text available:  pdf(751.77 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

A high speed mechanism for process exchange is essential in a time sharing system based on the use of many processes. The automated process exchange mechanism on the Model P400 and the 50 Series of PR1ME machines is described. Typical timing for operations using the process exchange mechanism on the P750 is given. Its use in PRIMOS is explained and validated.

20 Special issue: dasCMP'05: Hardware-modulated parallelism in chip multiprocessors

 Julia Chen, Philo Juang, Kevin Ko, Gilberto Contreras, David Penry, Ram Rangan, Adam Stoler, Li-Shiuan Peh, Margaret Martonosi

November 2005 **ACM SIGARCH Computer Architecture News**, Volume 33 Issue 4

Publisher: ACM Press

Full text available:  pdf(509.03 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Chip multi-processors (CMPs) already have widespread commercial availability, and technology roadmaps project enough on-chip transistors to replicate tens or hundreds of current processor cores. How will we express parallelism, partition applications, and schedule/place/migrate threads on these highly-parallel CMPs? This paper presents and evaluates a new approach to highly-parallel CMPs, advocating a new hardware-software contract. The software layer is encouraged to expose large amounts of mult ...

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1 ["Topologies"—distributed objects on multicomputers](#)

Karsten Schwan, Win Bo

May 1990 **ACM Transactions on Computer Systems (TOCS)**, Volume 8 Issue 2

Publisher: ACM Press

Full text available: [pdf\(3.83 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Application programs written for large-scale multicomputers with interconnection structures know the programmer (e.g., hypercubes or meshes) use complex communication structures for connecting the applications' parallel tasks. Such structures implement a wide variety of functions, including the exchange of data or control information relevant to the task computations and/or the communications required for task synchronization, message forwarding/filtering under program control, and so on ...

2 [A "flight data recorder" for enabling full-system multiprocessor deterministic replay](#)

Min Xu, Rastislav Bodik, Mark D. Hill

May 2003 **ACM SIGARCH Computer Architecture News, Proceedings of the 30th annual international symposium on Computer architecture ISCA '03**, Volume 31 Issue 2

Publisher: ACM Press

Full text available: [pdf\(311.95 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Debuggers have been proven indispensable in improving software reliability. Unfortunately, on real-life software, debuggers fail to deliver their most essential feature --- a faithful replay of the execution. The reason is non-determinism caused by multithreading and non-repeatable inputs. A common solution to faithful replay has been to record the non-deterministic execution. Existing recorders, however, either work only for datarace-free programs or have prohibitive overhead. A step tow ...

3 [A design flow for partially reconfigurable hardware](#)

Ian Robertson, James Irvine

May 2004 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 3 Issue 2

Publisher: ACM Press

Full text available: [pdf\(698.30 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper presents a top-down designer-driven design flow for creating hardware that exploits partial run-time reconfiguration. Computer-aided design (CAD) tools are presented, which complement conventional FPGA design environments to enable the specification, simulation, bo...

functional and timing), synthesis, automatic placement and routing, partial configuration generation and control of partially reconfigurable designs. Collectively these tools constitute the dynamic circuit switching CAD f ...

Keywords: FPGA, Viterbi decoder, configuration control, dynamically reconfigurable logic (DRL), power estimation, run-time reconfiguration (RTR)

4 A distributed, operating system based, blackboard architecture for real-time control

 Daniel L. Larner

June 1990 **Proceedings of the 3rd international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1 IEA/AIE '90**

Publisher: ACM Press

Full text available:  pdf(1.26 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The design and implementation of a Distributed, Operating System based, Blackboard Architecture for Real-Time control (DOSBART) is described. DOSBART demonstrates the outstanding applicability of AI languages and blackboard techniques to the construction of versatile distributed real-time control frameworks. It provides a means to remotely and transparently execute operations on non-local objects, furnishing the ability to share data and blackboard structures transparently across a network ...

5 A framework for the assessment of operating systems for small computers

 Hossein Saiedian, Munib Siddiqi

April 1996 **ACM SIGICE Bulletin**, Volume 21 Issue 4

Publisher: ACM Press

Full text available:  pdf(1.89 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A number of high performance operating systems are now available for small computers on different hardware platforms. These operating systems offer many advanced features formerly reserved for their workstation and minicomputer counterparts. This article surveys the most widely used of such operating systems, namely OS/2, Windows NT, Linux and Macintosh System 7.5. It provides an account on the history, design objectives and evolution of these operating systems and discusses their key features, ...

Keywords: CP/M, DOS, Linux, Macintosh, Microcomputers, OS/2, Operating Systems, Small Computer Systems, Windows, Windows NT

6 A history of the Promis technology: an effective human interface

 Jan Schultz

January 1986 **Proceedings of the ACM Conference on The history of personal workstations**

Publisher: ACM Press

Full text available:  pdf(2.61 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Scientific computing systems for individuals were pioneered early at Hewlett-Packard, beginning with the 9100A Desktop Calculator in 1968. Extensions of this first machine were soon seen in Personal Peripherals, such as Printers, Tape Cartridges, and Plotters, and followed by Graphic CRT Displays. By early 1972, the Desktop unit had been augmented by a very powerful Pocket Calculator, the ground-breaking HP 35A. This paper traces the evolution of these machines to the present day,

7 A multi-disciplinary approach for digital systems design curricula

 William T. Neumann, Marvin C. Woodfill

March 1992 **ACM SIGCSE Bulletin , Proceedings of the twenty-third SIGCSE technical symposium on Computer science education SIGCSE '92**, Volume 24 Issue 1

Publisher: ACM Press

Full text available:  pdf(554.09 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Historically, Digital Systems Design curricula have focused principally on the hardware design aspects of systems development. At Arizona State University, we have expanded our Digital Systems Design program to include aspects of computer architecture, assembly language programming, and operating systems fundamentals, as well. We feel that this larger focus provides the students with a more comprehensive understanding of the overall system development process. In this paper, we discuss our philosophy ...

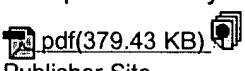
8 A Multi-Level Design Flow for Incorporating IP Cores: Case Study of 1D Wavelet IP Integration

Adel Baganne, Imed Bennour, Mehrez Elmarzougui, Riadh Gaiech, Eric Martin

March 2003 **Proceedings of the conference on Design, Automation and Test in Europe: Designers' Forum - Volume 2 DATE '03**

Publisher: IEEE Computer Society

Full text available:



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The design of high performance multimedia systems in a short time force us to use IP's blocks in many designs. However, their correct integration in a design implies more complex verification problems. In this paper, we present a C++/SystemC based simulation flow at multiple levels of abstraction. Our approach is to use SystemC to describe both application and a set of algorithm cores to be incorporated throughout the design flow. Our methodology supports design refinement through four main ...

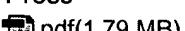
9 A proposal for certain process management and intercommunication primitives

 Gary D. Knott

January 1975 **ACM SIGOPS Operating Systems Review**, Volume 9 Issue 1

Publisher: ACM Press

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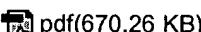
10 A reconfigurable dual output low power digital PWM power converter

 Abram Dancy, Anantha Chandrakasan

August 1998 **Proceedings of the 1998 international symposium on Low power electronics and design**

Publisher: ACM Press

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Most work to date on power reduction has focused at the component level, not at the system level. In this paper, we propose a framework for describing the power behavior of system-level designs. The model consists of a set of resources, an environmental workload specification, and a power management policy, which serves as the heart of the system model. We map this model to a simulation-based framework to obtain an estimate of the system's power dissipation. Accompanying ...

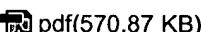
11 A SMART scheduler for multimedia applications

 Jason Nieh, Monica S. Lam

May 2003 **ACM Transactions on Computer Systems (TOCS)**, Volume 21 Issue 2

Publisher: ACM Press

Full text available:



Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Real-time applications such as multimedia audio and video are increasingly populating the workstation desktop. To support the execution of these applications in conjunction with traditional non-real-time applications, we have created SMART, a Scheduler for Multimedia And Real-Time applications. SMART supports applications with time constraints, and provides dynamic feedback to applications to allow them to adapt to the current load. In addition, the support for real-time

applications is integrat ...

Keywords: Scheduling, multimedia, proportional sharing, real-time

12 A study of initialization in Linux and OpenBSD

 Catherine Dodge, Cynthia Irvine, Thuy Nguyen
April 2005 **ACM SIGOPS Operating Systems Review**, Volume 39 Issue 2

Publisher: ACM Press

Full text available:  pdf(2.02 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The code that initializes a system can be notoriously difficult to understand. In secure systems, initialization is critical for establishing a starting state that is secure. This paper explores two architectures used for bringing an operating system to its initial state, once the operating system gains control from the boot loader. Specifically, the ways in which the OpenBSD and Linux operating systems handle initialization are dissected.

13 A survey of commercial parallel processors

 Edward Gehringer, Janne Abullarade, Michael H. Gulyen
September 1988 **ACM SIGARCH Computer Architecture News**, Volume 16 Issue 4

Publisher: ACM Press

Full text available:  pdf(2.96 MB)

Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

This paper compares eight commercial parallel processors along several dimensions. The processors include four shared-bus multiprocessors (the Encore Multimax, the Sequent Balance system, the Alliant FX series, and the ELXSI System 6400) and four network multiprocessors (the BBN Butterflies, the NCUBE, the Intel iPSC/2, and the FPS T Series). The paper contrasts the computers from the standpoint of interconnection structures, memory configurations, and interprocessor communication. Also, the share ...

14 A survey of extensions to APL

 Karl Fritz Ruehr
July 1982 **ACM SIGAPL APL Quote Quad , Proceedings of the international conference on APL '82**, Volume 13 Issue 1

Publisher: ACM Press

Full text available:  pdf(3.57 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A survey of proposed extensions to the APL language is made with emphasis placed on the motivations for various proposals, the differences between them and the consequences of their adoption. Some issues of a more general nature concerning the purpose, process and direction of language extension are also discussed. An extensive bibliography is provided with annotations concerning the nature, development and influence of various authors' works. Areas of extension encompassed by the survey in ...

15 A Survey of Some Theoretical Aspects of Multiprocessing

 J. L. Baer
March 1973 **ACM Computing Surveys (CSUR)**, Volume 5 Issue 1

Publisher: ACM Press

Full text available:  pdf(4.05 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

16 A Survey of Techniques for Synchronization and Recovery in Decentralized Computer Systems

 Walter H. Kohler
June 1981 **ACM Computing Surveys (CSUR)**, Volume 13 Issue 2

Publisher: ACM Press

Full text available:  pdf(3.33 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

17 A system for computer music performance

 David P. Anderson, Ron Kuivila

February 1990 **ACM Transactions on Computer Systems (TOCS)**, Volume 8 Issue 1

Publisher: ACM Press

Full text available:  pdf(2.21 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
[review](#)

A computer music performance system (CMPS) is a computer system connected to input device (including musical keyboards or other instruments) and to graphic and audio output devices. A human performer generates input events using the input devices. The CMPS responds to these events by computing and performing sequences of output actions whose intended timing is determined algorithmically. Because of the need for accurate timing of output actions, the scheduling requirements of a CMPS differ ...

18 A taxonomy of computer program security flaws

 Carl E. Landwehr, Alan R. Bull, John P. McDermott, William S. Choi

September 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 3

Publisher: ACM Press

Full text available:  pdf(3.81 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
[review](#)

An organized record of actual flaws can be useful to computer system designers, programmers, analysts, administrators, and users. This survey provides a taxonomy for computer program security flaws, with an Appendix that documents 50 actual security flaws. These flaws have all been described previously in the open literature, but in widely separated places. For those new to the field of computer security, they provide a good introduction to the characteristics of security flaws and how they ...

Keywords: error/defect classification, security flaw, taxonomy

19 altPm: a strategy for integrating IP with ATM

 Guru Parulkar, Douglas C. Schmidt, Jonathan S. Turner

October 1995 **ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Applications, technologies, architectures, and protocols for computer communication SIGCOMM '95**, Volume 25 Issue 4

Publisher: ACM Press

Full text available:  pdf(1.17 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes research on new methods and architectures that enable the synergistic combination of IP and ATM technologies. We have designed a highly scalable gigabit IP router based on an ATM core and a set of tightly coupled general-purpose processors. This altPm (pronounced "alt-PM" or, if you prefer, "ip-attem") architecture provides flexibility in congestion control, routing, resource management, and packet ...

20 Active base stations and nodes for wireless networks

Athanassios Boulis, Paul Lettieri, Mani Srivastava

January 2003 **Wireless Networks**, Volume 9 Issue 1

Publisher: Kluwer Academic Publishers

Full text available:  pdf(441.19 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Mobile and wireless network systems are characterized by a highly time varying and heterogeneous operational environment. For example, the wireless link bandwidth and bit error rate can change ...

to fading, mobile nodes may have different capabilities, and in the course of its movements a node may visit base stations that provide different sets of services, protocols, and interfaces. Adaptability, in various forms and at various levels of the system, is a key to combating the inh... va ...

Keywords: active networking, base station, reconfigurable hardware, wireless and mobile node

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An optimal algorithm for minimizing run-time reconfiguration delay

Full text [PDF](#) (1.83 MB)**Source** [ACM Transactions on Embedded Computing Systems \(TECS\) archive](#)Volume 3, Issue 2 (May 2004) [table of contents](#)

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Authors [Soheil Ghiasi](#) University of California, Los Angeles, CA
[Ani Nahapetian](#) University of California, Los Angeles, CA
[Majid Sarrafzadeh](#) University of California, Los Angeles, CA**Publisher** ACM Press New York, NY, USA**Additional Information:** [abstract](#) [references](#) [index terms](#) [collaborative colleagues](#) [peer to peer](#)**Tools and Actions:** [Discussions](#) [Find similar Articles](#) [Review this Article](#)
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↑ ABSTRACT

Reconfiguration delay is one of the major barriers in the way of dynamically adapting a system to its application requirements. The run-time reconfiguration delay is quite comparable to the application latency for many classes of applications and might even dominate the application run-time. In this paper, we present an efficient optimal algorithm for minimizing the run-time reconfiguration (context switching) delay of executing an application on a dynamically adaptable system. The system is composed of a number of cameras with embedded reconfigurable resources collaborating in order to track an object. The operations required to execute in order to track the object are revealed to the system at run-time and can change according to a number of parameters, such as the target shape and proximity. Similarly, we can assume that the applications comprising tasks are already scheduled and each of them has to be realized on the reconfigurable fabric in order to be executed. The modeling and the algorithm are both applicable to partially reconfigurable platforms as well as multi-FPGA systems. The algorithm can be directly applied to minimize the application run-time for the typical classes of applications, where the actual execution delay of the basic operations is negligible compared to the reconfiguration delay. We prove the optimality and the efficiency of our algorithm. We report the experimental results, which demonstrate a 2.5--40% improvement on the total run-time reconfiguration delay as compared to other heuristics.

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↑ INDEX TERMS

Primary Classification:

B. Hardware

- ↪ B.8 Performance and Reliability
- ↪ B.8.2 Performance Analysis and Design Aids

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Algorithms, Performance

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1 [Fast detection of communication patterns in distributed executions](#)

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

Publisher: IBM Press

Full text available:  [pdf\(4.21 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

2 [Highly available systems for database applications](#)



Won Kim
March 1984 **ACM Computing Surveys (CSUR)**, Volume 16 Issue 1

Publisher: ACM Press

Full text available:  [pdf\(2.43 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

As users entrust more and more of their applications to computer systems, the need for systems that are continuously operational (24 hours per day) has become even greater. This paper presents a survey and analysis of representative architectures and techniques that have been developed for constructing highly available systems for database applications. It then proposes a design of a distributed software subsystem that can serve as a unified framework for constructing database applica ...

3 [IS '97: model curriculum and guidelines for undergraduate degree programs in information systems](#)



Gordon B. Davis, John T. Gorgone, J. Daniel Couger, David L. Feinstein, Herbert E. Longenecker

December 1996 **ACM SIGMIS Database , Guidelines for undergraduate degree programs on Model curriculum and guidelines for undergraduate degree programs in information systems IS '97**, Volume 28 Issue 1

Publisher: ACM Press

Full text available:  [pdf\(7.24 MB\)](#) Additional Information: [full citation](#), [citations](#)

4 Distributed operating systems

 Andrew S. Tanenbaum, Robbert Van Renesse
December 1985 **ACM Computing Surveys (CSUR)**, Volume 17 Issue 4

Publisher: ACM Press

Full text available:  pdf(5.49 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Distributed operating systems have many aspects in common with centralized ones, but they also differ in certain ways. This paper is intended as an introduction to distributed operating systems, and especially to current university research about them. After a discussion of what constitutes a distributed operating system and how it is distinguished from a computer network, various key design issues are discussed. Then several examples of current research projects are examined in some detail ...

5 Experience Using Multiprocessor Systems—A Status Report

 Anita K. Jones, Peter Schwarz
June 1980 **ACM Computing Surveys (CSUR)**, Volume 12 Issue 2

Publisher: ACM Press

Full text available:  pdf(4.48 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

6 Xunet 2: lessons from an early wide-area ATM testbed

Charles R. Kalmanek, Srinivasan Keshav, William T. Marshall, Samuel P. Morgan, Robert C. Restrick

February 1997 **IEEE/ACM Transactions on Networking (TON)**, Volume 5 Issue 1

Publisher: IEEE Press

Full text available:  pdf(231.69 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

Keywords: asynchronous transfer mode, available bit rate, constant bit rate, variable bit rate

7 Experiences in integrating distributed shared memory with virtual memory

 management
R. Ananthanarayanan, Sathis Menon, Ajay Mohindra, Umakishore Ramachandran
July 1992 **ACM SIGOPS Operating Systems Review**, Volume 26 Issue 3

Publisher: ACM Press

Full text available:  pdf(1.56 MB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

While the duality between message-passing and shared memory for interprocess communication is well-known, the shared memory paradigm has drawn considerable attention in recent times even in distributed systems. Distributed Shared Memory (DSM) is the abstraction for supporting the notion of shared memory in a physically non-shared (distributed) architecture. It gives a uniform set of mechanisms for accessing local and remote memories. Further, by combining shared memory style synchronization with ...

8 Cellular disco: resource management using virtual clusters on shared-memory multiprocessors

 Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum
August 2000 **ACM Transactions on Computer Systems (TOCS)**, Volume 18 Issue 3

Publisher: ACM PressFull text available:  pdf(287.05 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Despite the fact that large-scale shared-memory multiprocessors have been commercially available for several years, system software that fully utilizes all their features is still not available, mostly due to the complexity and cost of making the required changes to the operating system. A recently proposed approach, called Disco, substantially reduces this development cost by using a virtual machine monitor that leverages the existing operating system technology. In this paper we present a ...

Keywords: fault containment, resource management, scalable multiprocessors, virtual machines

9 Promises and reality: Performance measurements of a user-space DAFS server with a database workload 

 Samuel A. Fineberg, Don WilsonAugust 2003 **Proceedings of the ACM SIGCOMM workshop on Network-I/O convergence: experience, lessons, implications****Publisher:** ACM PressFull text available:  pdf(366.48 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We evaluate the performance of a user-space Direct Access File System (DAFS) server and Oracle Disk Manager (ODM) client using two synthetic test codes as well as the Oracle database. Tests were run on 4-processor Intel Xeon-based systems running Windows 2000. The systems were connected with ServerNet II, a Virtual Interface Architecture (VIA) compliant system area network. We compare the performance of DAFS/ODM and local-disk based I/O, measuring I/O bandwidth and latency. We also compare the r ...

Keywords: DAFS, Database, File Systems, I/O, Networks, Performance Evaluation, RDMA

10 Minipage locking support for object-oriented page-server DBMS 

 S. Iris Chu, Marianne WinslettNovember 1994 **Proceedings of the third international conference on Information and knowledge management****Publisher:** ACM PressFull text available:  pdf(1.01 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Many object-oriented database systems are implemented using a page-server architecture for its performance advantages. Since the applications envisioned for object-oriented DBMSes typically spend a great deal of time processing data already in memory, fast in-memory access is very important. A page-server architecture will permit an implementation where most routine reference following (i.e., where the referenced data is in memory and appropriately locked) is handled by virtual memory hardw ...

11 Cellular Disco: resource management using virtual clusters on shared-memory multiprocessors 

 Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel RosenblumDecember 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth ACM symposium on Operating systems principles SOSP '99, Volume 33 Issue 5****Publisher:** ACM PressFull text available:  pdf(1.93 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)

terms

Despite the fact that large-scale shared-memory multiprocessors have been commercially available for several years, system software that fully utilizes all their features is still not available, mostly due to the complexity and cost of making the required changes to the operating system. A recently proposed approach, called Disco, substantially reduces this development cost by using a virtual machine monitor that leverages the existing operating system technology. In this paper we present a syste ...

12 Experiences with VI communication for database storage

 Yuanyuan Zhou, Angelos Bilas, Suresh Jagannathan, Cezary Dubnicki, James F. Philbin, Kai Li

May 2002 **ACM SIGARCH Computer Architecture News , Proceedings of the 29th annual international symposium on Computer architecture ISCA '02 , Proceedings of the 29th annual international symposium on Computer architecture ISCA '02**, Volume 30 Issue 2

Publisher: IEEE Computer Society, ACM Press

Full text available:  [pdf\(1.29 MB\)](#)  Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
[Publisher Site](#)

This paper examines how VI-based interconnects can be used to improve I/O path performance between a database server and the storage subsystem. We design and implement a software layer, DSA, that is layered between the application and VI. DSA takes advantage of specific VI features and deals with many of its shortcomings. We provide and evaluate one kernel-level and two user-level implementations of DSA. These implementations trade transparency and generality for performance at different degrees ...

Keywords: Storage system, cluster-based storage, Database storage, storage area network, User-level Communication, Virtual Interface Architecture, processor overhead

13 CARAT: a testbed for the performance evaluation of distributed database systems

Walt Kohler, Bao-Chyuan Jenq

November 1986 **Proceedings of 1986 ACM Fall joint computer conference**

Publisher: IEEE Computer Society Press

Full text available:  [pdf\(1.21 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

14 Activity-based computing: support for mobility and collaboration in ubiquitous computing

E: Bardram

September 2005 **Personal and Ubiquitous Computing**, Volume 9 Issue 5

Publisher: Springer-Verlag

Full text available:  [pdf\(412.31 KB\)](#) Additional Information: [full citation](#), [abstract](#)

This paper presents the design philosophy of *activity-based computing* (ABC), which addresses mobility and cooperation in human work activities. Furthermore, it presents the ABC framework, which is a ubiquitous computing infrastructure supporting ABC. The idea of ABC and the aim of the ABC framework is to: (1) support human activity by managing its collection of work tasks on a computer, (2) support mobility by distributing activities across heterogeneous computing environments, (3) suppor ...

Keywords: Activity-based computing, Computer supported cooperative work, Context-aware computing, Pervasive healthcare, State management, Ubiquitous computing

15 Towards distributed programming paradigms in Ada 9X

 Anthony Gargaro

June 1993 **Proceedings of the tenth annual Washington Ada symposium on Ada: Ada's role in software engineering**

Publisher: ACM Press

Full text available:  pdf(1.29 MB)

Additional Information: [full citation](#), [references](#), [citations](#)



16 A model for recentralization of computing: (distributed processing comes home)

 Harold Lorin

March 1990 **ACM SIGARCH Computer Architecture News**, Volume 18 Issue 1

Publisher: ACM Press

Full text available:  pdf(1.38 MB)

Additional Information: [full citation](#), [abstract](#), [index terms](#)

Distributed systems commonly contain heterogeneity at all levels of systems structure, differentiated by function (special servers), operating systems and architecture within a single system. On the other hand, large mainframes tend to be more homogeneous in their structures, even when they are multiprocessors. This paper explores a way of using the models of heterogeneous distributed computing within a mainframe. The argument is that appropriate restructuring of the mainframe can achieve a conv ...

17 Chiron-1: a software architecture for user interface development, maintenance, and run-time support

 Richard N. Taylor, Kari A. Nies, Gregory Alan Bolcer, Craig A. MacFarlane, Kenneth M. Anderson, Gregory F. Johnson

June 1995 **ACM Transactions on Computer-Human Interaction (TOCHI)**, Volume 2 Issue 2

Publisher: ACM Press

Full text available:  pdf(2.65 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)



The Chiron-1 user interface system demonstrates key techniques that enable a strict separation of an application from its user interface. These techniques include separating the control-flow aspects of the application and user interface: they are concurrent and may contain many threads. Chiron also separates windowing and look-and-feel issues from dialogue and abstract presentation decisions via mechanisms employing a client-server architecture. To separate application code from user interf ...

Keywords: artists, client-server, concurrency, event-based integration, user interface architectures

18 Understanding fault-tolerant distributed systems

 Flavin Cristian

February 1991 **Communications of the ACM**, Volume 34 Issue 2

Publisher: ACM Press

Full text available:  pdf(6.17 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)



19 A survey of commercial parallel processors

 Edward Gehringer, Janne Abullarade, Michael H. Gulyn

September 1988 **ACM SIGARCH Computer Architecture News**, Volume 16 Issue 4

Publisher: ACM Press



Full text available:  pdf(2.96 MB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

This paper compares eight commercial parallel processors along several dimensions. The processors include four shared-bus multiprocessors (the Encore Multimax, the Sequent Balance system, the Alliant FX series, and the ELXSI System 6400) and four network multiprocessors (the BBN Butterfly, the NCUBE, the Intel iPSC/2, and the FPS T Series). The paper contrasts the computers from the standpoint of interconnection structures, memory configurations, and interprocessor communication. Also, the share ...

20 RAID: high-performance, reliable secondary storage

 Peter M. Chen, Edward K. Lee, Garth A. Gibson, Randy H. Katz, David A. Patterson
June 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 2

Publisher: ACM Press

Full text available:  pdf(3.60 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Disk arrays were proposed in the 1980s as a way to use parallelism between multiple disks to improve aggregate I/O performance. Today they appear in the product lines of most major computer manufacturers. This article gives a comprehensive overview of disk arrays and provides a framework in which to organize current and future work. First, the article introduces disk technology and reviews the driving forces that have popularized disk arrays: performance and reliability. It discusses the tw ...

Keywords: RAID, disk array, parallel I/O, redundancy, storage, striping

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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = HEINRICH

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09092681</u>	<u>5948090</u>	150	06/05/1998	METHOD AND APPARATUS FOR CONTROLLING RESET OF COMPONENT BOARDS IN A COMPUTER SYSTEM	HEINRICH, DAVID
<u>10037501</u>	Not Issued	41	01/04/2002	Method and apparatus for emulating an OS-supported communication device to enable remote debugging	HEINRICH, DAVID
<u>10037541</u>	<u>7047462</u>	150	01/04/2002	METHOD AND APPARATUS FOR PROVIDING JTAG FUNCTIONALITY IN A REMOTE SERVER MANAGEMENT CONTROLLER	HEINRICH, DAVID
<u>10037680</u>	Not Issued	71	01/04/2002	Method and apparatus for creating a secure embedded I/O processor for a remote server management controller	HEINRICH, DAVID
<u>06926000</u>	Not Issued	168	10/31/1986	FROZEN CONFECTION AND METHOD FOR MAKING SAME	HEINRICH, DAVID B.
<u>07265761</u>	Not Issued	161	11/01/1988	FROZEN CONFECTION WITH A "FLAVOR CORE" COLUMN AND METHOD FOR MAKING FROZEN CONFECTION PRODUCTS	HEINRICH, DAVID B.
<u>08206388</u>	<u>5435143</u>	150	03/04/1994	MACHINE AND METHOD FOR MAKING HARDENED CONFECTIONS HAVING COMPLEMENTARY PARTS JOINED IN A UNIT	HEINRICH, DAVID B.
<u>08226494</u>	<u>5447036</u>	150	04/12/1994	ROTARY MACHINE AND METHOD FOR MAKING HARDENED CONNECTIONS	HEINRICH, DAVID B.

				OF COMPLEMENTARY PARTS	
<u>08452148</u>	5568729	150	05/26/1995	MACHINE AND METHOD FOR MAKING HARDENED CONFECTIONS HAVING COMPLEMENTARY PARTS JOINED IN A UNIT	HEINRICH, DAVID B.
<u>09532109</u>	6249830	150	03/21/2000	Method and apparatus for distributing interrupts in a scalable symmetric multiprocessor system without changing the bus width or bus protocol	HEINRICH, DAVID F.
<u>10178981</u>	7028215	150	06/25/2002	HOT MIRRORING IN A COMPUTER SYSTEM WITH REDUNDANT MEMORY SUBSYSTEMS	HEINRICH, DAVID F.
<u>10179001</u>	7035953	150	06/25/2002	COMPUTER SYSTEM ARCHITECTURE WITH HOT PLUGGABLE MAIN MEMORY BOARDS	HEINRICH, DAVID F.
<u>10348181</u>	Not Issued	61	01/21/2003	Method and apparatus for adding main memory in computer systems operating with mirrored main memory	HEINRICH, DAVID F.
<u>10393191</u>	6975136	150	03/20/2003	ISOLATED CHANNEL IN AN INTEGRATED CIRCUIT	HEINRICH, DAVID F.
<u>10444154</u>	Not Issued	20	05/22/2003	Mechanisms to prevent undesirable bus behavior	HEINRICH, DAVID F.
<u>11209527</u>	Not Issued	20	08/23/2005	Method and apparatus for capturing video data to a virtual screen buffer	HEINRICH, DAVID F.
<u>11209886</u>	Not Issued	20	08/23/2005	Method and apparatus for redirection of video data	HEINRICH, DAVID F.
<u>11209897</u>	Not Issued	20	08/23/2005	Method and apparatus for managing changes in a virtual screen buffer	HEINRICH, DAVID F.
<u>11209943</u>	Not Issued	30	08/23/2005	Method and apparatus for capturing slices of video data	HEINRICH, DAVID F.
<u>11210082</u>	Not Issued	25	08/23/2005	Method and apparatus for capturing and transmitting screen images	HEINRICH, DAVID F.
<u>60377863</u>	Not Issued	159	05/03/2002	Advanced memory protection	HEINRICH, DAVID F.
<u>60603796</u>	Not	159	08/23/2004	Method and apparatus for	HEINRICH, DAVID

	Issued			providing graphical data	F.
<u>08366509</u>	<u>5627962</u>	150	12/30/1994	CIRCUIT FOR REASSIGNING THE POWER-ON PROCESSOR IN A MULTIPROCESSING SYSTEM	HEINRICH, DAVID F.
<u>08699912</u>	Not Issued	167	08/20/1996	METHOD AND SYSTEM FOR SUPPORTING BUS AGENTS IN A SYMMETRIC MULTIPROCESSING ENVIRONMENT	HEINRICH, DAVID F.
<u>09033192</u>	<u>6370649</u>	150	03/02/1998	COMPUTER ACCESS VIA A SINGLE-USE PASSWORD	HEINRICH, DAVID F.
<u>09047789</u>	<u>6199167</u>	150	03/25/1998	COMPUTER ARCHITECTURE WITH PASSWORD-CHECKING BUS BRIDGE	HEINRICH, DAVID F.
<u>09070937</u>	<u>6177808</u>	150	04/30/1998	INTEGRATION OF BIDIRECTIONAL SWITCHES WITH PROGRAMMABLE LOGIC	HEINRICH, DAVID F.
<u>09196549</u>	<u>6460139</u>	150	11/20/1998	APPARATUS AND METHOD FOR PROGRAMMABLY AND FLEXIBLY ASSIGNING PASSWORDS TO UNLOCK DEVICES OF A COMPUTER SYSTEM INTENDED TO REMAIN SECURE	HEINRICH, DAVID F.
<u>09196800</u>	<u>6542995</u>	150	11/20/1998	APPARATUS AND METHOD FOR MAINTAINING SECURED ACCESS TO RELOCATED PLUG AND PLAY PERIPHERAL DEVICES	HEINRICH, DAVID F.
<u>09196849</u>	<u>6510522</u>	150	11/20/1998	APPARATUS AND METHOD FOR PROVIDING ACCESS SECURITY TO A DEVICE COUPLED UPON A TWO-WIRE BIDIRECTIONAL BUS	HEINRICH, DAVID F.
<u>09227510</u>	<u>6263395</u>	150	01/06/1999	SYSTEM AND METHOD FOR SERIAL INTERRUPT SCANNING	HEINRICH, DAVID F.
<u>09243235</u>	<u>6041377</u>	150	02/02/1999	METHOD AND APPARATUS FOR DISTRIBUTING INTERRUPTS IN A SCALABLE SYMMETRIC MULTIPROCESSOR SYSTEM WITHOUT CHANGING THE BUS WIDTH OR BUS PROTOCOL	HEINRICH, DAVID F.

<u>60604769</u>	Not Issued	159	08/26/2004	Screening assays for inhibitors of the SirABC-mediated iron transport system of staphylococcus aureus	HEINRICHS, DAVID
<u>60607896</u>	Not Issued	159	09/08/2004	Screening assays for biosynthetic inhibitors of a novel Staphylococcus aureus siderophore	HEINRICHS, DAVID
<u>09719155</u>	6570006	150	06/29/2001	A BACTERIAL GENE AND METHOD OF TREATING A GRAM NEGATIVE BACTERIAL INFECTION	HEINRICHS, DAVID E.
<u>10414197</u>	Not Issued	161	04/16/2003	Bacterial gene and method of treating a gram negative bacterial infection	HEINRICHS, DAVID E.
<u>60621921</u>	Not Issued	159	10/25/2004	Staphylococcus aureus Isd protein-based anti-infectives	HEINRICHS, DAVID E.
<u>60089189</u>	Not Issued	159	06/12/1998	METHOD OF TREATING A GRAM NEGATIVE BACTERIAL INFECTION	HEINRICHS, DAVID E.
<u>60095037</u>	Not Issued	159	08/03/1998	NOVEL GENE AND METHOD OF TREATMENT GRAM NEGATIVE BACTERIAL INFECTION	HEINRICHS, DAVID E.
<u>60096026</u>	Not Issued	159	08/10/1998	NOVEL GENE AND METHOD OF TREATING A GRAM NEGATIVE BACTERIAL INFECTION	HEINRICHS, DAVID E.

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Your Search was:

Last Name = NOONAN

First Name = ROBERT

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09256823	Not Issued	160	02/24/1999	METHOD AND APPARATUS FOR USING EDO MEMORY DEVICES IN A MEMORY SYSTEM DESIGNED FOR FPM MEMORY DEVICES	NOONAN II, ROBERT W.
08683900	Not Issued	161	07/19/1996	FREESTANDING EXERCISE APPARATUS	NOONAN, ROBERT
09305386	6496904	150	05/05/1999	METHOD AND APPARATUS FOR EFFICIENT TRACKING OF BUS COHERENCY BY USING A SINGLE COHERENCY TAG BANK	NOONAN, ROBERT
08167088	5505432	150	12/16/1993	"FISH TAPES" AND ANTI-SNAGGING DEVICES THEREFOR	NOONAN, ROBERT J.
10006878	6857040	150	12/05/2001	BI-DIRECTIONAL BUS BRIDGE IN WHICH MULTIPLE DEVICES CAN ASSERT BUS CONCURRENTLY	NOONAN, ROBERT L.
10037502	Not Issued	41	01/04/2002	Method and apparatus for passive PCI throttling in a remote server management controller	NOONAN, ROBERT L.
10037680	Not Issued	71	01/04/2002	Method and apparatus for creating a secure embedded I/O processor for a remote server management controller	NOONAN, ROBERT L.
10038009	6819322	150	01/04/2002	METHOD AND APPARATUS FOR DETECTING POTENTIAL LOCK-UP CONDITIONS IN A VIDEO GRAPHICS CONTROLLER	NOONAN, ROBERT L.
10444154	Not Issued	20	05/22/2003	Mechanisms to prevent undesirable bus behavior	NOONAN, ROBERT L.

<u>11209527</u>	Not Issued	20	08/23/2005	Method and apparatus for capturing video data to a virtual screen buffer	NOONAN, ROBERT L.
<u>11209886</u>	Not Issued	20	08/23/2005	Method and apparatus for redirection of video data	NOONAN, ROBERT L.
<u>11209897</u>	Not Issued	20	08/23/2005	Method and apparatus for managing changes in a virtual screen buffer	NOONAN, ROBERT L.
<u>11209943</u>	Not Issued	30	08/23/2005	Method and apparatus for capturing slices of video data	NOONAN, ROBERT L.
<u>11210082</u>	Not Issued	25	08/23/2005	Method and apparatus for capturing and transmitting screen images	NOONAN, ROBERT L.
<u>60603796</u>	Not Issued	159	08/23/2004	Method and apparatus for providing graphical data	NOONAN, ROBERT L.
<u>05627695</u>	4235837	150	10/31/1975	ORIENTED CONTAINER	NOONAN, ROBERT P.
<u>06070265</u>	4220252	150	08/27/1979	BIOLOGICAL SPECIMEN PROCESS APPARATUS	NOONAN, ROBERT P.
<u>06257512</u>	4347222	250	04/21/1981	GAS GENERATING APPARATUS FOR ANAEROBIC ATMOSPHERE	NOONAN, ROBERT P.
<u>09812346</u>	6549420	150	03/20/2001	METHOD AND APPARATUS FOR INCREASING MEMORY CAPACITY	NOONAN, ROBERT W.
<u>08741699</u>	6310782	150	10/31/1996	AN APPARATUS FOR MAXIMIZING MEMORY DENSITY WITHIN EXISTING COMPUTER SYSTEM FORM FACTORS	NOONAN, ROBERT W.
<u>09387079</u>	Not Issued	160	08/31/1999	METHOD AND APPARATUS FOR INCREASING MEMORY CAPACITY	NOONAN, ROBERT W.
<u>08775315</u>	6034919	150	12/31/1996	METHOD AND APPARATUS FOR USING EXTENDED DATA OUTPUT MEMORY DEVICES IN A SYSTEM DESIGNED FOR FAST PAGE MODE MEMORY DEVICES	NOONAN,, ROBERT W.

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Inventor Name Search Result

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Last Name = EMERSON

First Name = THEODORE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09105725	6098143	150	06/25/1998	REMOTE SERVER MANAGEMENT DEVICE	EMERSON, THEODORE
09256587	6101617	150	02/22/1999	COMPUTER FAILURE RECOVERY AND SYSTEM ALERT	EMERSON, THEODORE F
09932541	7003775	150	08/17/2001	HARDWARE IMPLEMENTATION OF AN APPLICATION-LEVEL WATCHDOG TIMER	EMERSON, THEODORE F.
10006878	6857040	150	12/05/2001	BI-DIRECTIONAL BUS BRIDGE IN WHICH MULTIPLE DEVICES CAN ASSERT BUS CONCURRENTLY	EMERSON, THEODORE F.
10037501	Not Issued	41	01/04/2002	Method and apparatus for emulating an OS-supported communication device to enable remote debugging	EMERSON, THEODORE F.
10037502	Not Issued	41	01/04/2002	Method and apparatus for passive PCI throttling in a remote server management controller	EMERSON, THEODORE F.
10037541	7047462	150	01/04/2002	METHOD AND APPARATUS FOR PROVIDING JTAG FUNCTIONALITY IN A REMOTE SERVER MANAGEMENT CONTROLLER	EMERSON, THEODORE F.
10037680	Not Issued	71	01/04/2002	Method and apparatus for creating a secure embedded I/O processor for a remote server management controller	EMERSON, THEODORE F.
10037931	7038696	150	01/04/2002	METHOD AND APPARATUS FOR IMPLEMENTING COLOR GRAPHICS ON A REMOTE	EMERSON, THEODORE F.

COMPUTER					
<u>10037984</u>	<u>6774904</u>	150	01/04/2002	OPERATING SYSTEM INDEPENDENT METHOD AND APPARATUS FOR GRAPHICAL REMOTE ACCESS HAVING IMPROVED LATENCY	EMERSON, THEODORE F.
<u>10038009</u>	<u>6819322</u>	150	01/04/2002	METHOD AND APPARATUS FOR DETECTING POTENTIAL LOCK-UP CONDITIONS IN A VIDEO GRAPHICS CONTROLLER	EMERSON, THEODORE F.
<u>10106279</u>	Not Issued	123	03/26/2002	Method and apparatus for storing data and replaying stored data in a temporally accurate manner	EMERSON, THEODORE F.
<u>10108106</u>	<u>6742066</u>	150	03/27/2002	METHOD FOR CONTROLLING REMOTE CONSOLE FUNCTIONALITY ASSIST LOGIC	EMERSON, THEODORE F.
<u>10108207</u>	Not Issued	168	03/27/2002	Computer system having remote console functionality assist logic	EMERSON, THEODORE F.
<u>10145553</u>	Not Issued	161	05/14/2002	Replacement, upgrade and/or addition of hot-pluggable components in a computer system	EMERSON, THEODORE F.
<u>10444154</u>	Not Issued	20	05/22/2003	Mechanisms to prevent undesirable bus behavior	EMERSON, THEODORE F.
<u>10611403</u>	Not Issued	30	07/01/2003	Operating system independent method and apparatus for graphical remote access	EMERSON, THEODORE F.
<u>10715960</u>	Not Issued	30	11/18/2003	Generating pointer position data from position data of a pointing device of a remote console	EMERSON, THEODORE F.
<u>10716144</u>	Not Issued	71	11/18/2003	System and method for controlling remote console functionality assist logic	EMERSON, THEODORE F.
<u>11168129</u>	Not Issued	20	06/28/2005	Centralized hot-pluggable video and KVM	EMERSON, THEODORE F.
<u>11209527</u>	Not Issued	20	08/23/2005	Method and apparatus for capturing video data to a virtual screen buffer	EMERSON, THEODORE F.
<u>11209579</u>	Not Issued	30	08/23/2005	System and method for interacting with a remote computer	EMERSON, THEODORE F.
<u>11209886</u>	Not Issued	20	08/23/2005	Method and apparatus for redirection of video data	EMERSON, THEODORE F.

<u>11209897</u>	Not Issued	20	08/23/2005	Method and apparatus for managing changes in a virtual screen buffer	EMERSON, THEODORE F.
<u>11209943</u>	Not Issued	30	08/23/2005	Method and apparatus for capturing slices of video data	EMERSON, THEODORE F.
<u>11210082</u>	Not Issued	25	08/23/2005	Method and apparatus for capturing and transmitting screen images	EMERSON, THEODORE F.
<u>11294862</u>	Not Issued	41	12/05/2005	Method and apparatus for implementing color graphics on a remote computer	EMERSON, THEODORE F.
<u>60377863</u>	Not Issued	159	05/03/2002	Advanced memory protection	EMERSON, THEODORE F.
<u>60603796</u>	Not Issued	159	08/23/2004	Method and apparatus for providing graphical data	EMERSON, THEODORE F.
<u>07307404</u>	4942606	150	02/07/1989	COMPUTER WITH IMPROVED KEYBOARD PASSWORD FUNCTIONS	EMERSON, THEODORE F.
<u>07955849</u>	5390324	150	10/02/1992	COMPUTER FAILURE RECOVERY AND ALERT SYSTEM	EMERSON, THEODORE F.
<u>08303405</u>	5574864	150	09/09/1994	METHOD OF IMPLEMENTING EISA BUS DEVICES ON A HOST BUS BY DISABLING BRIDGE CIRCUITRY BETWEEN HOST AND EISA BUSES	EMERSON, THEODORE F.
<u>08390499</u>	5596711	150	01/26/1995	COMPUTER FAILURE RECOVERY AND ALERT SYSTEM	EMERSON, THEODORE F.
<u>08733254</u>	6476854	150	10/18/1996	VIDEO EAVESDROPPING AND REVERSE ASSEMBLY TO TRANSMIT VIDEO ACTION TO A REMOTE CONSOLE	EMERSON, THEODORE F.
<u>08733415</u>	5790895	150	10/18/1996	MODEM SHARING	EMERSON, THEODORE F.
<u>08733521</u>	5898861	150	10/18/1996	TRANSPARENT KEYBOARD HOT PLUG	EMERSON, THEODORE F.
<u>08768681</u>	5956475	150	12/18/1996	COMPUTER FAILURE RECOVERY AND ALERT SYSTEM	EMERSON, THEODORE F.
<u>08971009</u>	Not Issued	161	11/14/1997	COMPUTER PERIPHERAL CONTROL APPARATUS AND ASSOCIATED METHOD	EMERSON, THEODORE F.

<u>08988345</u>	<u>6212587</u>	150	12/10/1997	DEVICE PROXY AGENT FOR HIDING COMPUTING DEVICES ON A COMPUTER BUS	EMERSON, THEODORE F.
<u>09086690</u>	Not Issued	161	05/28/1998	METHOD, SYSTEM, AND APPARATUS FOR INTELLEGENT INPUT/OUTPUT DEVICE DRIVER TRANSLATION, AND EMULATION	EMERSON, THEODORE F.
<u>09140040</u>	<u>6173341</u>	150	08/26/1998	SYSTEM AND METHOD FOR CONFIGURING ADAPTERS IN A COMPUTER SYSTEM	EMERSON, THEODORE F.
<u>09303180</u>	<u>6401157</u>	150	04/30/1999	HOT-PLUGGABLE COMPONENT DETECTION LOGIC	EMERSON, THEODORE F.
<u>09303369</u>	<u>6487623</u>	150	04/30/1999	REPLACEMENT, UPGRADE AND/OR ADDITION OF HOT-PLUGGABLE COMPONENTS IN A COMPUTER SYSTEM	EMERSON, THEODORE F.
<u>09313220</u>	<u>6385682</u>	150	05/17/1999	SYSTEM AND METHOD FOR CONTROLLING REMOTE CONSOLE FUNCTIONALITY ASSIST LOGIC	EMERSON, THEODORE F.
<u>09438253</u>	<u>6664969</u>	150	11/12/1999	OPERATING SYSTEM INDEPENDENT METHOD AND APPARATUS FOR GRAPHICAL REMOTE ACCESS	EMERSON, THEODORE F.

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